Docket No.: GR 00 P 1031

AUR 0 4 2005

The AFF

I hereby certify that this correspondence is sping deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addresses the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22318-1450, on the date indicated below.

By May 15 ----

Date: August 2, 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No.

09/760,405

Confirmation No: 1766

Applicant

Xiaoning Nie

Filed

January 12, 2001

Title

Data-Processing Device for Processing Two or More

Independent Processes in Parallel

Art Unit

2183

Examiner

Aimee J. Li

Docket No.

GR 00 P 1031

Customer No. :

24131

<u>INFORMATION DISCLOSURE STATEMENT</u> <u>UNDER 37 C.F.R. 1.97(d)</u>

Hon. Commissioner for Patents

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

International Publication WO 99/31580 (Akkary et al.), dated June 24, 1999;

Steven Wallace et al.: "Threaded Multiple Path Execution", 1998 IEEE, XP-002322675, 12 pgs.;

Quinn Jacobson et al. "Control Flow Speculation in Multiscalar Processors", Proceedings of the Third International Symposium on High Performance Computer Architecture, San Antonio TX, February 1-5, 1997, pp. 1-13;

European Search Report dated June 27, 2005.

08/04/2005 HGUTEMA1 00000037 09760405

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In accordance with 37 C.F.R. 1.97(e) the undersigned herewith states that each item of information contained in the information disclosure statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement.

In accordance with 37 C.F.R. 1.97 (d), consideration of this Information Disclosure Statement is requested.

Enclosed is the fee in the amount of \$180.00.

Respectfully submitted,

Date: August 1, 2005

For Applicant

Lerner and Greenberg, P.A. Post Office Box 2480 Hollywood, FL 33022-2480

Tel: (954) 925-1100 Fax: (954) 925-1101

/am

Alfred K. Dassler

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U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT (37 CFR 1.98(b))				Attorney Docket No.: GR 00 P 1031		Applic. No. 09/760,405		
				Applicant				
				Xiaoning Nie				
				Filing Date Group Art Unit				
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		U.S.	PATENT I	DOCUMENTS	•			
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	0	Steven Wallace e 002322675, 12 pg		aded Multiple Path E	ecution",	1998 IEE	E, XP	
	P	Quinn Jacobson et al. "Control Flow Speculation in Multiscalar Processors", Proceedings of the Third International Symposium on High Performance Computer Architecture, San Antonio TX, February 1-5, 1997, pp. 1-13						

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

DATE CONSIDERED

EXAMINER